

IN THE CLAIMS

1. (Original) A method for formal automated signal analysis upon elements of a design of a electronic circuit, comprising:

categorizing a given one of said elements into one of a plurality of types;

initiating a technique for characterizing the immunity of said given element to electrical signal effects, said technique appropriate for said categorized type, said technique yielding results capable of being verified by non-automated signal analysis; and

determining, based on said characterizing, whether a signal integrity violation will occur as a result of said given element, and if a violation will occur, how said violation could be repaired.

Cancel Claims 2-23.

60389841v1